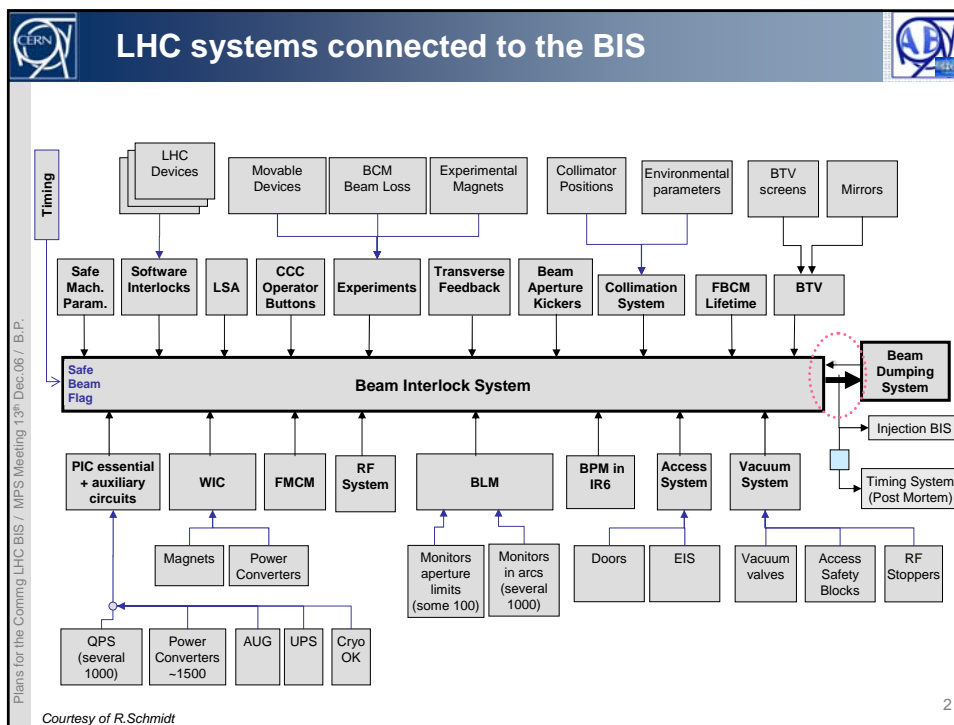
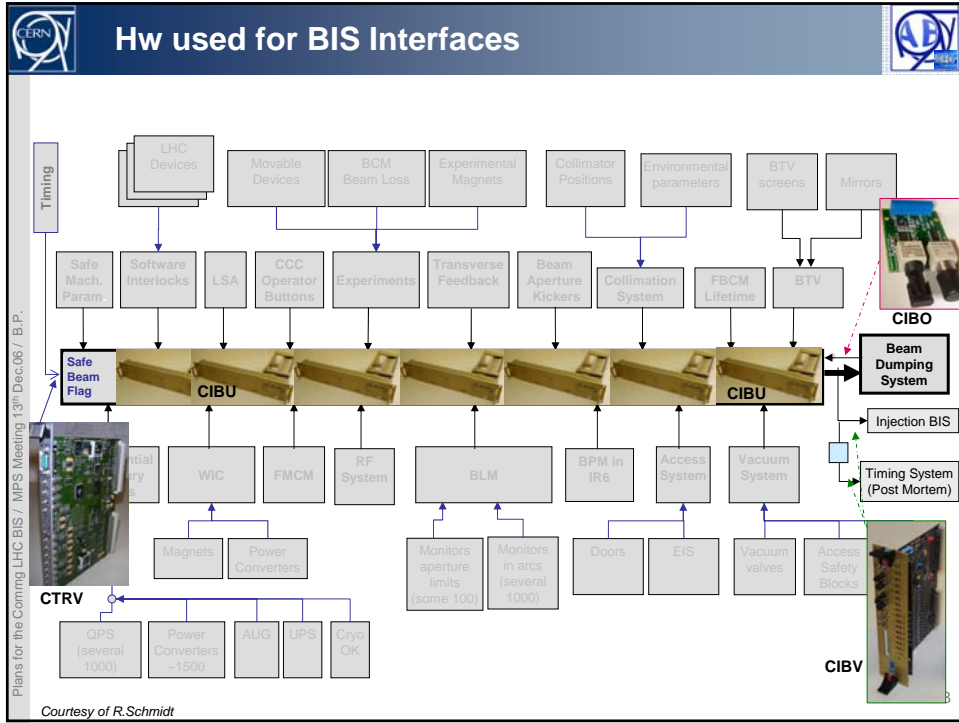


LHC Beam Interlock System: Interfaces & Commissioning plan

Plans for the Commg LHC BIS / MPS Meeting 13th Dec.06 / B.P.

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Distribution of the BIS Connections

User System name	IR1		IR2			IR3			IR4			IR5			IR6			IR7			IR8			CCR		
	L1	L1	R1	R1	L2	R2	L2	R2	U3	S3	L4	R4	L5	R5	L6	R6	U7	S7	L8	R8	L0	R0	L0	R0		
Collimators	42	1	1	2	2	2	2	2	2				2	2	2	2	2		2	2		2	2	2		
PIC	32	2	2	2	2	2	2	4			2	2	2	2	2	2	4			2	2					
Vacuum	32	1	1	1	1	1	1	1		1	1	1	1	1	1	1	1	1		1	1	1	1	1		
BLM	16								2	2			2	2					2	2						
FMCM	12	1				2			3					1					2							
WIC	8	1			1			1				1	1		1				1					1		
BTV	9			1				1	1			1	1			1	1	1						1		
MKD, MKI & MKQA	6									1	1	1				1			1			1		1		
RF system	4									1	1	1														
ATLAS / LCHf	5			3																						
CMS / Totem	5			1										5												
ALICE	2					2								1												
LHCb	3																						3			
Transverse feedback	2										1	1														
Beam life time	2										1	1														
Beam excursion	2															1	1									
Access system	1																						1			
LHC Operator Buttons	2																						1	1		
Software Interlock	2																						1	1		
LSA	2																						1	1		
Safe Machine Param.	2																						1	1		
Nbr of connections:	191	24		26			18			23			24			23			17			27			9	
		IR1		IR2			IR3			IR4			IR5			IR6			IR7			IR8			CCR	

Plans for the Coming LHC BIS / MPS Meeting 13th Dec.06 / B.P.

Plans for the Commg LHC BIS / MPS Meeting 13th Dec.06 / B.P.

BIS Commissioning Plan

The BIS commissioning will be composed of several phases:

- 1) Controllers & Beam Permit Loops test
- 2) Individual System Test (User Interfaces)

- 3) Hw Test with all User Systems connected to the BIS
("BIS Sequencer" is expected)
- 4) Additional check including the Beam Dumping System
▲("double" arming sequence)

Functional Test User Systems / BIS

- 5) Without beam
- 6) With beam

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Plans for the Commg LHC BIS / MPS Meeting 13th Dec.06 / B.P.

Reminder & Pending Questions

There will be also 4 other BIS to commission:

- **TT60/Ti2** & (TT40/Ti8)
- **INJ1 & INJ2**

We have to be prepared if:

- Some User systems (NOT maskable ones) will NOT be ready on time.
- "BIS Sequencer" do not have full coverage of the BIS connections

On the MP point of view, what are the relevant ways to check the different connections *User systems => BIS* ?

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